EFFECT OF SINTERING TEMPERATURE ON V$_2$O$_5$ DOPED ZnO-Bi$_2$O$_3$-Sb$_2$O$_3$-MnO$_2$ BASED VARISTOR CERAMICS: MICROSTRUCTURE AND ELECTRICAL PROPERTIES

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In this study the effect of the sintering temperature (1200–1300 °C) on V$_2$O$_5$ doped ZnO–Bi$_2$O$_3$-Sb$_2$O$_3$-MnO$_2$ (ZBSM) based varistor ceramics from 0 to 0.4 mol% was investigated for electrical and microstructural properties. The materials was processed using a conventional solid-state method and the finished samples were characterized using XRD, SEM and EDX techniques. The measured leakage current density was 1×10$^{-4}$ mA/cm$^2$ when the barrier height was 1.38 eV, which gave the highest nonlinear coefficient around 12.18 at 0.20 mol%. The analyses related to those quoted in the literatures confirmed the presence of Zn$_7$Sb$_2$O$_{12}$, MnVO$_4$, BiVO$_4$, and Zn$_3$(VO$_4$)$_2$ polymorphs as the secondary phase with ZnO as the primary phase. The Zn$_3$(VO$_4$)$_2$ polymorphs resided at the triple point junctions or were embedded in ZnO grains. Henceforth, the results showed that V$_2$O$_5$ doping can facilitate the sintering process of the varistor ceramics, particularly in the phase formation. This contributed to better electrical properties, especially to 0.2 mol%. The density of the sintered ceramic decreased from 5.32 to 5.06 g/cm$^3$ and the average grain size increased in the range of 11.02 to 31.39 μm with an increased of sintering temperature from 1200 to 1300 °C, respectively.

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1. Introduction

Zinc oxide (ZnO) based varistors are electronic semiconducting ceramic materials that possess high energy absorption capability to protect electronic appliances against an unwanted voltage surge transmitted into the electronic components that can possibly cause damage [1]. They are simply achieved by sintering a ZnO powder mix with some minor metallic oxides. Moreover, ZnO varistor materials form a polycrystalline structure after sintering, consisting of semiconductor ZnO grains [2]. The type of additives used is greatly important in it’s fabrication, leading to the formation of secondary phases at the grain boundary that trigger the J-E characteristics [3].

Generally, the electrical properties, such as nonlinear coefficient (α), leakage current density ($J_L$), breakdown field ($E_b$), and barrier height ($\phi_b$) are controlled by the structure of the
grains and grain boundaries, mostly originating from the segregation of heavy metal oxide, such as Bi₂O₃, Pr₂O₃ and V₂O₃ on ZnO grains or grain boundary [4-5]. The nonlinear current density-electric field (J-E) characteristics of a ZnO based varistor is determined from the total number of grain-boundary layers that can be generated during the sintering process, including the formation of the electronic ϕₜ [6]. Consequently, the addition of minor oxides of MnO₂ in small percentages can avoid Bi₂O₃ and V₂O₃ evaporation during the sintering process [7]; also, Sb₂O₃ addition leads to the formation of a Zn₇Sb₂O₁₂ spinel phase which reduces the mobility of the grain boundaries and regulates the ZnO grain growth [8-9].

Recent studies have revealed that V₂O₅ along with a varistor previously added on ZnO has the advantage in terms of lowering the sintering temperature, but there is no improvement in the electrical properties. There is a need for incorporation of many additives to improve the α of the varistor [10, 11, 12]. Accordingly, the combined effects of MnO₂, Pr₂O₃, CoO₃ and Sb₂O₃ on the ZnO–V₂O₅ contributes to the varistor Eₛ potential, which acts from highly resistive to extremely conductive; also, the ceramic can be sintered at a high temperature of above 1250 °C [12, 13, 14].

Although, the effect of the sintering temperature on V₂O₅ doped ZnO-Bi₂O₃-Sb₂O₃-MnO₂ (ZBSM) based varistor ceramics has not been studied symmetrically, there has been a report in literature on the combined effect of ZnO-V₂O₅-MnO₂-Nb₂O₅-Bi₂O₃ ceramics, recently [15]. In addition the combined effect of ZnO-V₂O₃-MnO₂-Nb₂O₅ ceramics was reported [16]. Mn₁₂O₃ doping in ZnO-V₂O₃ varistor ceramics was also reported to control ZnO abnormal grain growth [17]. As reported earlier, incorporation of V₂O₅ additives markedly enhanced the densification rate of the ZnO materials [18]. Also, the grain-growth rate of the ZnO materials was significantly increased because V₂O₅ acted as a liquid phase sintering.

Generally, V₂O₅ improved the electrical properties in particular the nonlinear J-E characteristics of ZnO based varistor ceramics [19]. Another important parameter to be considered is the microstructural properties in which the varistor electrical properties can be determined, particularly the J-E characteristics of the ZnO based varistor ceramics. The responsible mechanism is the grain boundary; better grain boundaries produce optimum nonlinear J-E characteristics [20-21].

Recently, the investigation on the effect of V₂O₅ doped ZnO at 0.5 mol% and low sintering temperature has received major attention in specific the degradation behavior, electrical properties and microstructure [22, 23, 24]. However, many problems related to ZnO based varistors such as abnormal grain boundaries need to be solved in order to use them for commercial purposes. The selected type of sintering process for a specific composition is needed in order to improve the α of ZnO-V₂O₅ based ceramics [22]. As a result, it is significant to investigate how a small amount of V₂O₅, between 0.08 to 0.4 mol%, will significantly affect the performance of ZnO varistor properties in different sintering temperatures.

Numerous processing conditions have been established to improve the microstructural and electrical properties of ZnO varistor ceramics; several studies discovered that had a combined effect of ZnO–V₂O₅–MnO₂ varistor ceramics sintered between 1200-1300 °C for 4 hrs and the holding time did not give any nonlinear electrical properties [19, 25]. The effect of the sintering temperature on the additive of V₂O₅ commonly found in the well-studied ZBSM is still not reported in literatures.

In this work, a study on the effect of sintering temperature on V₂O₅ doped on ZBSM based varistor ceramics has been systematically studied for different sintering temperatures, and for its dependence on electrical and microstructure properties. It is unique compared with those previous works in term of the processing conditions; sintering temperature, sintering time, and additives. Here, the sintering temperature, time and selected of V₂O₅ doping used were >1200 °C, 4 h, and 0.2 mol%, respectively, compared to that of others were 900 °C, 10 min, and 0.5 mol% [18]. We expected that the electrical properties, particularly α, would increase to an optimum with the increase of sintering temperature and doping.
2. Experimental procedure

A high purity of metal oxide powders were obtained from Sigma-Aldrich; ZnO (99.9%), V₂O₅ (99.6%), Bi₂O₃ (99.98%), Sb₂O₃ (99.6%) and MnO₂ (86.93%), and the design ceramic system is ZnO₉₈₋ₓV₂O₅ₓBi₂O₃₀.₇MnO₂₀.₇Sb₂O₃₀.₃ where x is varied from 0.08 to 0.4 mol% at chosen sintering temperature 1250 °C. Later at an optimum x mol% of V₂O₅ in ZBSM ceramic the sintering temperature was varied between 1200 to 1300 °C to obtain further optimum in microstructure and electrical properties. The raw materials were mixed by ball-milling with zirconia balls in a polypropylene bottle for 24 h in acetone and a small addition of deionized water to avoid the sedimentation of heavy particles like Sb₂O₃. 0.75wt% polyvinyl alcohol as binder was added. The mixture was dried at 110 °C for 19 h in an oven and granulated using an agate mortar/pestle. The powder was sieved with a 75 micron mesh screen to produce the starting powder. The powder was pressed into discs (pellets) of 10 mm diameter and 1 mm thickness at a pressure of 50 MPa. The pellets were sintered in air in intervals of 4 h, with heating and cooling rates of 5 °C/min and finally, furnace-cooled to room temperature. The sintered samples were polished to a 0.89 mm thickness using SiC paper, P1200. For electroding, silver paste was coated on both side of its faces for areas of approximately 0.238 cm² and then heated at 550 °C for 12 min to form ohmic contacts.

2.1 Characterization method

The J-E characterization of the samples was recorded at room temperature by using a source measure unit (Keithley 2400). From it, the constant α was obtained by using the expression [26]:

\[ J = aE^\alpha \]  (1)

or

\[ \alpha = \frac{\log J_2 - \log J_1}{\log E_2 - \log E_1} \]  (2)

where the varistor electric fields E₂, E₁ were taken at current densities J₂ = 10 mA/cm², J₁ = 1 mA/cm², and the J₁ was evaluated at 0.85V₁mA.

Scanning electron microscope (SEM, JEOL JSM-6400) in conjunction with energy dispersive X-ray (EDX) was used to determine the morphological structure and elements within the composition. The XRD pattern of the prepared samples was recorded using (PANalytical X’Pert Pro PW3040/60, Philips). The samples were radiated with Ni-filtered CuKα radiation (λ = 1.5428 Å) within a 20 scan range of 20 - 80° to identify the crystalline phases; the data were analyzed using X’Pert High Score software. The density of the sintered pellets was measured with a digital electronic densitometer, and the average grain size was calculated by using the linear intercept method [27],[28].

\[ d = \frac{1.56L}{MN} \]  (3)

where L is the random line length on the micrograph, M is the micrograph magnification and N is the number of grain boundaries intercepted by lines.

3. Results and discussion

3.1 Phase identification

Fig. 1(a) shows the XRD pattern of phases obtained by the doping x of 0.08, 0.2, 0.4 mol% V₂O₅ composition in ZBSM ceramics sintered at a chosen sintering temperature which is 1250 °C. Fig. 1(b) is the pattern of sample produced from five sintering temperatures, 1200 to 1300 °C, for the selected x=0.2 mol% V₂O₅ based on the
optimum $\alpha$ from E-J characteristic. A strong peak related to the $\text{Zn}_3(\text{VO}_4)_2$ minor phase was detected. Usually, $\text{Zn}_3(\text{VO}_4)_2$ is found to be fixed in ZnO grains or at triple point junction grains [7, 24]. The existence of $\text{Zn}_3(\text{V}_4)_2$ could not be confirmed by XRD analysis at 1300 °C due to the volatility of the V-species; also, the main peak of $\text{Zn}_3(\text{VO}_4)_2$ had already overlapped with those of the $\text{Zn}_7\text{Sb}_2\text{O}_{12}$ spinel.

The trace for elevated temperatures revealed the presence of ZnO as the only primary phase; and, a few secondary phases, such as, $\text{BiVO}_4$, $\text{MnVO}_3$ and $\text{Zn}_3(\text{VO}_4)_2$ polymorphs, related to the V and $\text{Zn}_7\text{Sb}_2\text{O}_{12}$ species as the only secondary phases were detected. The presence of $\text{BiVO}_4$ shows that Bi and V-rich, a liquid phase sintering, has aided this result to be consistent with [15]. The formation of the $\text{Zn}_7\text{Sb}_2\text{O}_{12}$ spinel phase at 1200 °C, the initial temperature, until 1300 °C was found to control the abnormal ZnO grain growth and enhance the sintering process for the solubility of ions, such as the Zn and Bi-rich liquid phase, which were reported from literatures [26, 9]. The indexed spinel phase of $\text{Zn}_7\text{Sb}_2\text{O}_{12}$ obtained was a cubic structure with (ref. code: 00-014-0613). However, the XRD patterns show the presence of a MnVO$_3$ anorthic structure (ref. code 00-024-1260), $\text{Zn}_3(\text{VO}_4)_2$ in an orthorhombic structure (space group Pnca), with (ICDS ref. code: 00-034-0378), $\text{BiVO}_4$ Orthorhombic with (ref. code 00-012-0293) space group P nca. These are in addition to the major phase of the ZnO hexagonal structure.

3.2 Microstructure Characterizations

Fig. 2 (a to c) shows the microstructure of the sample with 0.08, 0.2 and 0.4 mol% of $\text{V}_2\text{O}_5$ content; all the ceramics were sintered at 1250 °C. The microstructure of the ceramics are well pronounced. Thus, the average grain size decreased gradually from 24.79 to 17.22 µm with increased in $\text{V}_2\text{O}_5$ content. This may likely be due to the presence of the $\text{Zn}_7\text{Sb}_2\text{O}_{12}$ spinel phase that can inhibit the ZnO grain growth [34]. The presence of pores decreased the relative density from 5.32 to 5.06 g/cm$^3$, for ceramic sintered at 1200 – 1300 °C may be this happen as a result of $\text{V}_2\text{O}_5$ volatility [12, 23] during sintering. Also, for 0.08 - 0.4 mol% $\text{V}_2\text{O}_5$ dopants sintered at 1250°C, maximum density of 5.12 g/cm$^3$ was observed from the ceramic doped with 0.2 mol% $\text{V}_2\text{O}_5$, see Table 1 below.

The EDX analysis confirmed the presence of Zn, Mn, V, Bi and Sb elements at the grain interior, grain boundaries and triple point junction coexisting in bulk Zn grains; Fig. 3, spectrum
(1, 2 and 3). Although it was reported that no V species was located in the ZnO grains’ interior [18].

![Microstructure of V$_2$O$_5$ doped ZBSM based ceramics](image)

**Fig. 2:** Microstructure of V$_2$O$_5$ doped ZBSM based ceramics (a) 0.08 mol%, (b) 0.2 mol% and (c) 0.4 mol% sintered at 1250 °C

![EDX spectra](image)

**Fig. 3:** EDX spectra for sample of 0.2 mol% of V$_2$O$_5$ content sintered at 1200 °C

Evidently form Fig. 4, a clear grain, with the exception of grain size, was monitored. The grain size and shape of the ceramics sintered at 1200 °C were different. The sample shows two different types of grains, the bulk grain ZnO phase and the inter-granular phase with defects concentrated on the surface. This result is consistent with the previous work [30]. During the sintering, the material was in thermodynamic equilibrium; so, there was a high Schottky type. Therefore, the defects formed on the surfaces on the bulk grains during cooling and annihilation [31]. The vanished defect concentration was mainly composed of V-species which were located at the triple junctions, this was confirmed by the EDX analysis in Fig. 4 (spectrum 1). When the temperature increased from 1250 to 1300 °C, the inter-granular phase disappearance detected and this is due to the high reactivity of the V-species at a higher temperature, which can be found in the grain boundary EDX analysis (spectrum 2). However, the average grain size increased to a maximum of 31.39 µm at 1300 °C. This was caused by the V-species that possibly melted easily on the ZnO grains interior leading to the exaggerated ZnO grains [32]. The exaggeration of the ZnO grain was potentially contributed to by the addition of V$_2$O$_5$ and its lower melting point (690 °C).
°C). This was a clear fact on the sample sintered at temperatures higher than the temperature at which the liquid phase occurred [33]. The presence of pores was attributed to the volatility of $V_2O_5$ at higher temperatures.

**3.3 Electrical Characterization**

Fig. 5(a) showed the J-E characteristic curve of sample sintered between 1200-1250 °C and doped at 0.2 mol% and Fig. 5(b) that of doped at 0.08, 0.2, 0.4 mol% and sintered at optimum temperature, 1250 °C. The electrical properties, such as $\alpha$, $\phi_b$, $J_L$, $E_b$, and microstructure properties, such as average grain size and relative density are summarized in Table 1.

The $\alpha$ results confirmed 1250 °C as the optimum sintering temperature with composition 0.2 mol% of $V_2O_5$ doped ZBSM ceramics sintered between 1200-1300 °C. However, at an optimum sintering temperature 1250 °C varying the dopants from 0.08 and 0.4 mol% the $\alpha$ does not significantly improved in comparison to 0.2 mol% of $V_2O_5$ doping. This means that sintering temperature 1250 °C and 0.2 mol% of $V_2O_5$ doped ZBSM is the optimum condition to achieve $\alpha$ value in the current process condition. The $\alpha$ value of 12.18 was accompanied by lowest $J_L$ around $1\times10^{-4}$ mA/cm$^2$ and a maximum $\phi_b$ of 1.38 eV; the larger the $\phi_b$ is the better is the $\alpha$ value [35]. Fig. 5(a) and (b) show the relation between $\phi_b$, $E_b$ and $\alpha$ as a function of temperature. Further increasing the sintering temperature caused $\alpha$ to decrease to 3.30 at 1300 °C. This was associated with the partial disappearance of the grain boundaries caused by a large growth of ZnO grains at a high temperature, which caused the reduction of $E_b$ to 12.10 V/mm and the $J_L$ to increase to a maximum value of $6\times10^{-5}$ mA/cm$^2$. The increase in grain size led to the retardation of the grain boundaries which can hinder it and cause a poor performance of the ZnO varistor ceramics [13 – 20]. Table (1) shows the electrical parameters for the samples with and without $V_2O_5$ contents. In
addition, the homogeneity of the sintered sample contributed to the variation of the electrical properties of the individual grain boundary, particularly at 1250 °C

Table 1: Electrical and microstructural properties for the V$_2$O$_5$ doped of ZBSM based varistor ceramics sintered at various temperatures and compositions.

<table>
<thead>
<tr>
<th>V$_2$O$_5$ doping (mol%)</th>
<th>Sintering temperature (°C)</th>
<th>d (µm)</th>
<th>ρ (g/cm$^3$)</th>
<th>$\phi_b$ (eV)</th>
<th>$E_b$ (V/mm)</th>
<th>$J_L$ (mA/cm$^2$)</th>
<th>α</th>
</tr>
</thead>
<tbody>
<tr>
<td>0.2</td>
<td>1200</td>
<td>12.02</td>
<td>5.32</td>
<td>0.53</td>
<td>22.90</td>
<td>3×10$^{-4}$</td>
<td>5.51</td>
</tr>
<tr>
<td></td>
<td>1225</td>
<td>14.87</td>
<td>5.28</td>
<td>0.65</td>
<td>60.50</td>
<td>2×10$^{-4}$</td>
<td>10.37</td>
</tr>
<tr>
<td></td>
<td>1250</td>
<td>20.99</td>
<td>5.22</td>
<td>1.38</td>
<td>50.20</td>
<td>1×10$^{-4}$</td>
<td>12.18</td>
</tr>
<tr>
<td></td>
<td>1275</td>
<td>22.81</td>
<td>5.11</td>
<td>0.47</td>
<td>39.20</td>
<td>2×10$^{-4}$</td>
<td>8.63</td>
</tr>
<tr>
<td></td>
<td>1300</td>
<td>31.39</td>
<td>5.06</td>
<td>0.46</td>
<td>12.10</td>
<td>6×10$^{-4}$</td>
<td>3.31</td>
</tr>
<tr>
<td>0.08</td>
<td>1250</td>
<td>23.09</td>
<td>5.02</td>
<td>1.00</td>
<td>56.95</td>
<td>1×10$^{-4}$</td>
<td>7.78</td>
</tr>
<tr>
<td>0.4</td>
<td>1250</td>
<td>17.22</td>
<td>5.12</td>
<td>1.03</td>
<td>32.0</td>
<td>1×10$^{-4}$</td>
<td>8.67</td>
</tr>
</tbody>
</table>

In addition, the homogeneity of the sintered ceramics contributed to the variation of the nonlinear electrical properties of the individual grain boundary, particularly at 1250 °C with 0.2 mol% V$_2$O$_5$ doped ZBMS [36]. It is a clear indicator that the nonlinear electrical properties of the ZnO varistor are influenced by its microstructure as shown in the SEM micrographs, Figs. 3 and 4.

Fig. 6: The relationship between (a) $E_b$, $\alpha$, and (b) $\phi_b$, $\alpha$, with sintered temperatures (1200-1300 °C).

Conclusion

The effect of the sintering temperature ranging from 1200 to 1300 °C and the composition of V$_2$O$_5$ (0.08 to 0.4 mol%) on the ZBSM based varistor ceramics was investigated. The results indicated that the optimum temperature that give the best in term of microstructure and electrical properties is at 1250 °C with 0.2 mol% of V$_2$O$_5$. Then by varying the dopant composition at this temperature it was observed that 0.2 mol% also give the optimum microstructure and electrical properties. The ceramic sintered at 1250 °C and 0.2 mol% of V$_2$O$_5$ exhibited the largest $\phi_b$, low $J_L$ with the best $\alpha$ around 12.18 and the $E_b$ of 50.20 V/mm.
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