

DESIGN OF OPTICAL TUNABLE CNOT (XOR) AND XNOR LOGIC GATES BASED ON 2D-PHOTONIC CRYSTAL CAVITY USING ELECTRO-OPTIC EFFECT

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We have proposed optical tunable *CNOT (XOR)* and *XNOR* logic gates using two-dimensional photonic crystal (*2DPhC*) cavities. Where, air rods with square lattice array have been embedded in Ag-Polymer substrate with refractive index of 1.59. In this work, we have enhanced speed of logic gates by applying two input signals with a phase difference at the same wavelength for *2DPhC* cavities. Where, we have adjusted the phases of input and control signals equal with $\pi/3$ and zero, respectively. The response times of the structure and quality factor of the cavities are in the range of femto second and 2000, respectively. Then, we have used electro-optic property of the substrate material to change the cavities resonance wavelengths. By this means, we could design the logic gates and demonstrate a tunable range of 23nm for their operation wavelength. The quality factor and the response times of cavities remain constant in the tunable range of wavelength, approximately. The evaluated least *ON* to *OFF* logic level contrast ratios for the *XOR* and *XNOR* logic gates are 25.45dB and 22.61dB, respectively. The bit rates of the proposed logic gates can reach up to higher than 0.166 P (*Peta*) bps values. According to the high rapid response time with acceptable quality factor of the *PhC* cavities, the proposed optical logic gates can be considered as appropriate candidates to be building blocks for applications such as optical integrated circuits and optical processors with an ultrahigh speed of data flow.

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1. Introduction

According to the reported works to date, in information processors and communication systems, employing ultrafast and cheap components provide them with competitive ability in market challenges. As a candidate to achieve this goal, *2DPhC* based all-optical devices (all optical logic gates) has led to significant increment in operating speed and consequently in the performance bandwidth of the systems. However, according to the recent reports, the response time of these logic gates (more of them) has been enhanced to picoseconds (*ps*) range [1-4]. Fushimi *et al.* designed scalable all-optical logic gates with the same wavelength of the input and output signals. They allowed a wavelength fluctuation, with 0.3 fractions of the cavity resonant wavelength widths. Then, they investigated cavities coupling to the waveguide and the operation degradation of logic gates. Also, Fushimi investigated the scalability of the designed logic gates considering their input power stability and tolerable fabrication errors [1]. Christina *et al.* proposed all optical *AND*, *NAND*, *XNOR* and *NOR* logic gates based on *2DPhCs* using self-collimation effect in a hexagonal structure by creating line defect in the structure [2]. Noshad *et al.* proposed *AND*, *NOR* and *NOT* all optical logic gates based on *2DPhCs*. They changed operational wavelength of the structure by embedding Kerr nonlinear rods in the structure [3]. Liu *et al.*

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proposed an all optical half adder based on cross structure *2DPhCs*. They first designed *AND* and *XOR* logic gates and then used them to make a half adder. They first designed an *AND* gate in a nonlinear structure and an *XOR* gate in another nonlinear structure and then combined them to make a half adder. Furthermore, they calculated an optimal operation speed without considering response time of the Kerr nonlinear material in the *ps* temporal range [4].

Using semiconductor micro-ring resonators is another alternative approach for all optical logic functions realizing [5, 6]. Bai *et al.* proposed all optical *NOT* and *NOR* logic gates by using a ring resonator. In this work they controlled the input signals by applying an optical probe wave to the ring. Where, wavelength of the probe and input signals were the same. Their logic gates were based on *2DPhCs* [7]. To the best of our knowledge, in the most of reported works, silicon rods have been used in the air or silica substrates but in among them some researchers have used ring resonators to perform logic gate functions. As instance, Andalib *et al.* could propose a controllable compact logic gate by using nonlinear ring resonators based on *2DPhCs* [8]. They could also propose all optical *AND* and *NOR* logic gates by their proposed nonlinear ring resonator [9, 10]. In spite of their claim, the proposed gates were big in size and consequently had low operational speed. Also, other alternative methods such as all optical logic gates designing by using ultra small *PhC* heterojunction diodes have been reported [11]. They analyzed all-passive on-chip optical *AND* and *NAND* logic gates made from a directional emitting cavity connecting two ultra-small *PhC* heterojunction diodes. Where, the analyzed logic gates were phase insensitive. However, proposed logic gates by Fu *et al.* were depend on phase of the input signals that created along the wave guides at the *2DPhC* structure [12].

Liu *et al.* proposed ultra-fast all optical *AND*, *NAND*, *OR* and *NOR* logic gates by using cavities based on *2DPhCs*, which operates with low input power and very low response times in around of femto second (*fs*) [13]. In this work, they utilized air rods in the Ag-Polymer substrate. By using the nonlinear property of Ag-Polymer they shifted the cavities resonance wavelength hand thereby could propose all optical logic gates. According to other studies, the structures made of Polyester based on *2DPhCs* have ultra-fast response times (*fs*) [14-16].

On the other hand, tunable *PhC* components are attractive subjects in the field of the modern optical communication systems extension. Tunable electro optical devices based on *2D* and also *3DPhC* structures have been proposed [17-21]. The experimental proof of an electro-optical *PhC* was investigated and found to be in agreement with numerical results [22]. Although photonic crystals can be employed to achieve low group velocities at their band edges, this is limited to a very narrow range of wave vectors in one particular direction. Recently, two-dimensional arrays of coupled photonic crystal resonators have been a study focus which exhibit reduced group velocities over the entire range of wave vectors [23, 24]. It is well known that the electro-optic effect has an ultra-low response time of the order of nanosecond. This property makes it highly desirable and opens up the possibilities for ultra-fast tunable *PhC* devices with low power. Yang *et al.* have investigated a novel-designed two-dimensional coupled photonic crystal resonator array (*2DCPCRA*) realized in *2DPhC* slabs filled with nonlinear polymer, which can dynamically tune the slow light properties in a wide frequency range and realize optical devices that could store and release optical pulses to implement values optical digital processing in future high speed optical networks [25].

In the present work, we used *2DPhCs* based cavities composed of air rods created in the Ag-polymer. It should be noted that we did not consider nonlinear properties of substrate (Ag-Polymer). The created phase shifts by the cavities was employed to propose all optical *CNOT* (*XOR*) and *XNOR* logic gates. This paper is composed of three sections. In the first section, we demonstrate and describe cavity, created in the *2DPhC*, and the cavities resonance wavelengths with investigation of electro-optical effects of that on the cavities. In the second section, simulation results and their discussions have been presented and the final section is devoted to the conclusion and the obtained research results.

Table 1. Resonance wavelength and quality factor for two cavity types without any voltage bias.

Cavity type	Resonate wavelength (λ_r)	Quality factor (Q)
L7	1561.1nm	1472.40
L8	1562.7nm	1816.86

2. Design and realization of structure

The employed cavity is created in a $2DPhC$ including air rods with $0.3a$ radii perforated in the Ag-Polymer substrate with refractive index of 1.59 and lattice constant of $a=550nm$. By removing of some central air rods an L -type cavity is created, where removing of seven or eight rods from a single row of the PhC results in $L7$ or $L8$ cavities, respectively. By decreasing radii of a line of nearest-neighbor rods of the cavity and also shifting them away, one can enhance the resonance wavelength (λ_r) and quality factor (Q) of the cavities. Here, we have decreased radii of a line of nearest-neighbor rods of the cavity to the value $0.25a$ and also shifted them and the second neighbor rods away in about $0.19a$ and $0.1a$, respectively. In table 1 we show the values of Q and λ_r for the cavities. Also, the schematic structure of cavity is depicted in Figure 1.

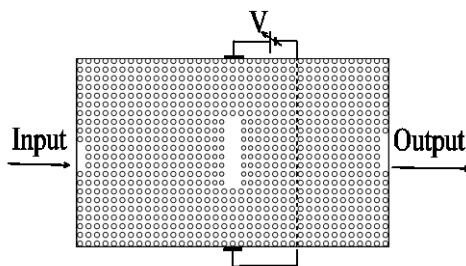


Fig.1. Schematically view of the L -type cavity structure in two-dimensional photonic crystals

As indicated in table 1, the Q and λ_r values are different for each cavity type, which increase with increasing cavity type. For the $L7$ cavity, the quality factor is equal to 1472 and for the $L8$ are 1816.86 . Also according to table 1, the resonance wavelength of the $L7$ and $L8$ cavities are $1561.1nm$ and $1562.7nm$, respectively.

By applying a single mode optical field to the $L7$ and $L8$ cavities, we can demonstrate that cavities resonate with 180 degrees phase difference. Normalized transmission spectrum profile for both of $L7$ and $L8$ cavity types are depicted in Fig, 2. According to the Fig, 2(a), it is observed that at wavelength $1561.8nm$, the transmitted electric field through $L7$ cavity has a positive value while the transmitted electric field through $L8$ cavity has a negative value. We employed this transmission characteristics of the cavities to propose ultra-fast all optical $CNOT$ (XOR) and $XNOR$ gates. The resonant optical fields within the $L7$ and $L8$ cavities experience a π radian phase difference by applying a Gaussian pulse with $1561.8nm$ central wavelength. The obtained phase difference cannot be retained through propagation in PhC due to the material impaction. Then, the phase difference changes from π radian to about 0.83π ($150 deg.$) at the output. To compensate this phase difference reduction, we applied the input signal of the $L7$ cavity with a $\pi/3$ radian extra phase. According to the observed Gaussian output pulses, we can see that the proposed structure operate at very high speed (fs) as illustrated in Fig, 3. In Fig, 2(a), one can observe that the resonance wavelengths of cavities blue shift by increasing the applied voltage. Where, a wavelength tunable range from $1561.8nm$ to $1538.8nm$ has been achieved by the applied voltage increasing from 0 to 3 volts (V). Fig. 4 shows the optical intensity of signals with and without the extra phase through the structure. Fig. 4 (a) and (c) show evolution of the phase difference of cavities from 180 to $150deg.$ through the structure in the cases V_{min} and V_{max} , respectively, for without the extra phase case.

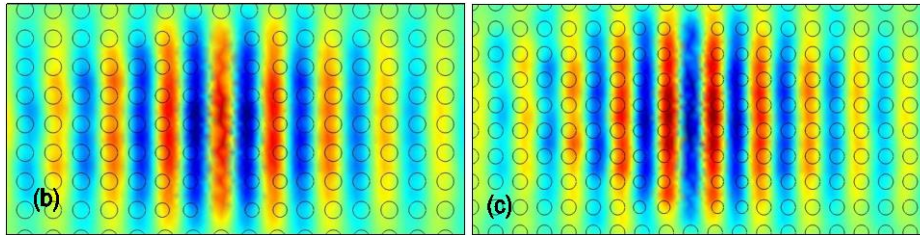
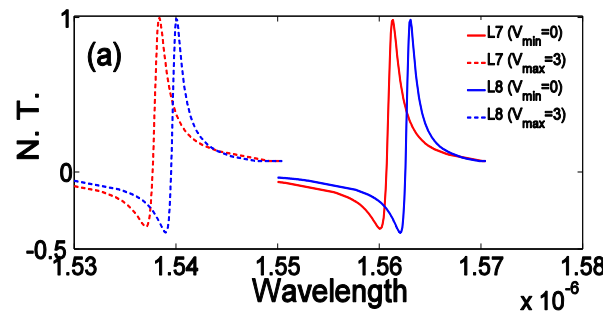


Fig. 2. a) Normalized transmission spectrum profile for the L7 and L8 cavities. b) Field distribution of the L7 cavity at wavelength of 1561.8nm. c) Field distribution of the L8 cavity at a wavelength in the tunable rang from 1561.8nm to 1538.8nm.

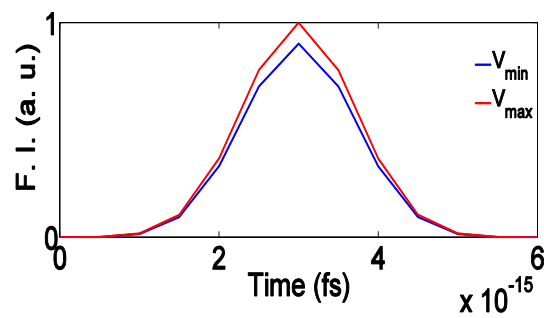


Fig. 3. Temporal view of the output pulse.

However, in the case of extra phase application ($\phi_{L7} = \pi/3$), Fig. 4(b) and (d) illustrate the evolution of the optical fields amplitudes for the cavities through the structure in the cases V_{min} and V_{max} , respectively, as it is observed the phase difference evolution reach to $180deg.$ in the output of structure. With comparing Fig. 4(a, c) and 4(b, d) one can demonstrate that $180deg.$ phase difference can be obtained in output, which is useful for realization of the claimed tunable optical logic gate.

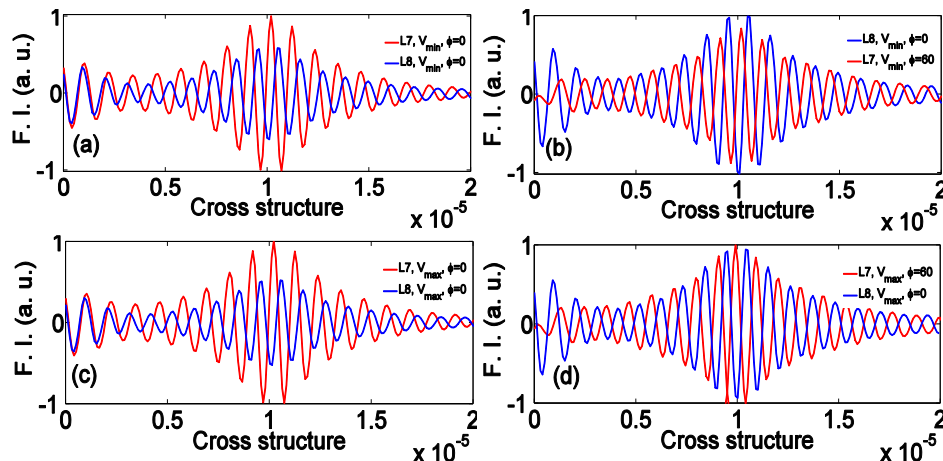


Fig. 4. Optical fields propagation through the structure at the extreme wavelengths, 1561.8nm (V_{min}) and 1538.8nm (V_{max}), of the tunable rang for a and c) without additional phase, i.e. $\varphi_{iL7} = 0$ and $\varphi_{iL8} = 0$; b and d) with additional phase of $\varphi_{iL7} = \pi/3$ while $\varphi_{iL8} = 0$.

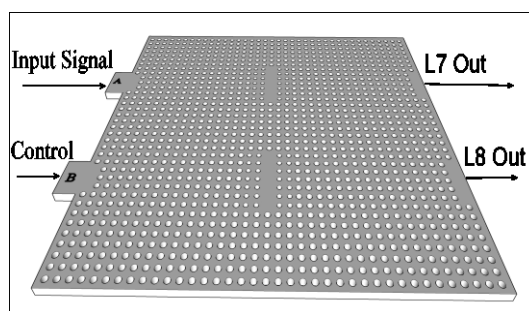


Fig. 5. Schematic illustration of the CNOT (XOR) gate.

3. CNOT and XNOR logic gates

3.1. CNOT (XOR) gate

To design a CNOT optical logic gate, we used two L7 and L8 cavities in 2DPhC with square lattice of air rods. We applied an input signal to the L7 cavity with phase of $\varphi_{iL7} = \pi/3$ as input for the CNOT gate and another input signal applied to the L8 cavity with input phase of $\varphi_{iL8} = 0$ as the control signal for the gate. Where, the field amplitude of control and input signals are equal. When the control field is set in zero "0" logic level, with no signal applied to the L8 cavity, the input signal of the L7 cavity propagates to the output without any considerable reduction in its amplitude, as depicted in Fig. 6(e, f) within temporal range of 12fs to 24fs. However, when the control field is set in the high "1" logic level, the reached pulse to the output of the gate is inverted of the input signal as depicted in Fig. 6(e, f) from 0fs to 12fs. Briefly, one can claim that no variation in the signal pulse amplitude is produced without applying any control signal. However, by applying a control signal, a π radian phase difference is produced between the input and control signals, which results in a destructive interference at the output and disappearing of the output signal referred as the low "0" logic level. As illustrated by Fig. 5, the input of L7 cavity is considered as the input port and the input of L8 cavity as the control input of the CNOT gate. The central wavelength of the both input pulses was the same at 1561.8nm. As explained in above, Figures 6(e, f) exhibits the cavities responses with Gaussian input pulses application for performing all logical cases of the truth table for the CNOT logic gate. As an alternative view, one can consider the performance of the illustrated structure as an XOR gate, due to the wavelength and field amplitude sameness of the applied input and control signals. Here, the evaluated least ON to OFF logic-level contrast ratio for the XOR logic gate is calculated as 25.45dB.

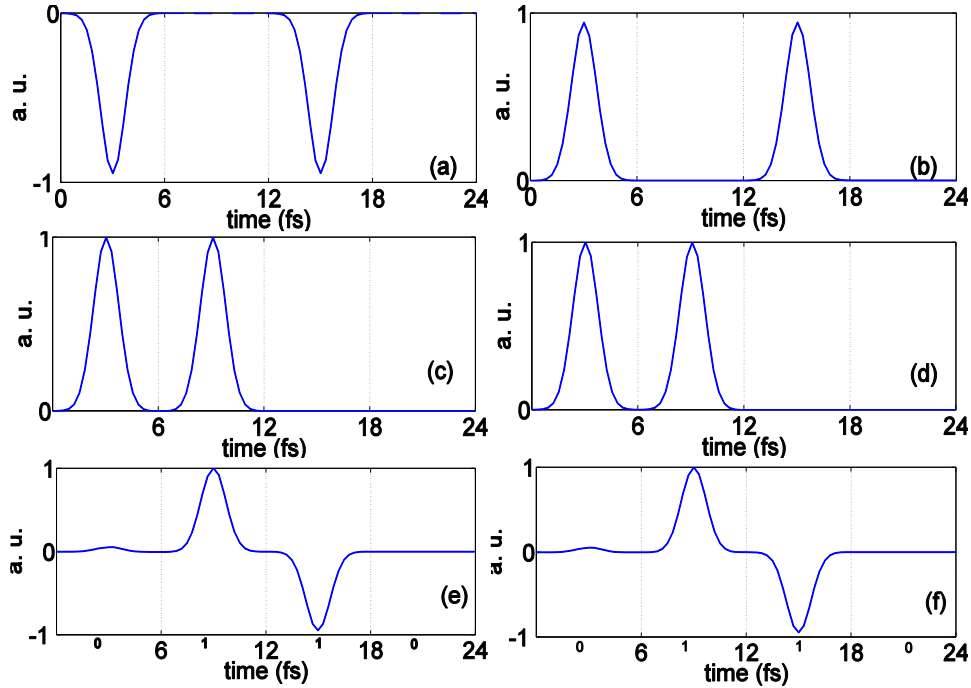


Fig. 6. Temporal view of the inputs and output signals for the CNOT (XOR) gate, a and b) amplitude and intensity of the input signal, c and d) amplitude and intensity of the control signal, e and f) amplitude and intensity of the output signal.

3.2. XNOR gate

In order to realize an XNOR gate, we should utilize three input signals where each of them is applied for one of three cavities created in the 2DPhC. Here, there are two L7 cavities to apply the input signals of the logic gate and one L8 cavity for applying the control signal. For the XNOR gate, always, the control field is set in high "1" logic level. When the input signals are set in low "0" logic levels, without applying any input signal to any L7 cavities, the applied control signal to the L8 cavity propagates to the output without any considerable variation in its amplitude, as exhibited in Fig. 7(g, h) within temporal range of 18fs to 24fs. However, when any of the input signals is set in the high "1" logic level, the received pulse to the output of the gate becomes zero, as depicted in Fig. 7(g, h) from 6fs to 18fs. As the last case, when both of the input signals are set in the high "1" logic level, interference of them with the control field cause in emerging an output pulse in the high "1" logic level as exhibited in Fig. 7(g, h) within the range of 0fs to 6fs. As the same as XOR, the evaluated least ON to OFF logic-level contrast ratio for the XNOR logic gate is obtained as 22.61dB. In this work, we have considered full width at half maximum (FWHM) of the applied Gaussian signals equal with 1fs. Where, we have calculated the bit rate limit of 0.166 peta bit per second (pbps) for temporal performance of the simulated logic gates. Then the proposed gates could demonstrate acceptable response for the Gaussian input pulses with FWHM value of 0.1fs, temporally. Thus, it is reasonable that data flow in the proposed logic gates can be enhanced to ultra high rates of 0.166pbps, approximately. In this section, we demonstrated the logic gates operation with minimum value of the applied voltage at the central wavelength of 1561.8nm. According to the Fig. 2(a) we simulated operation of the gates at both extreme wavelengths of the achieved 23nm tunable range. Thus, the designed tunable logic gates based on the electro-optic property of the substrate material show suitable characteristics. Also, we would mention that one can design optical tunable logic gates with employing only L7 or L8 cavities by applying different voltages for each of the cavities.

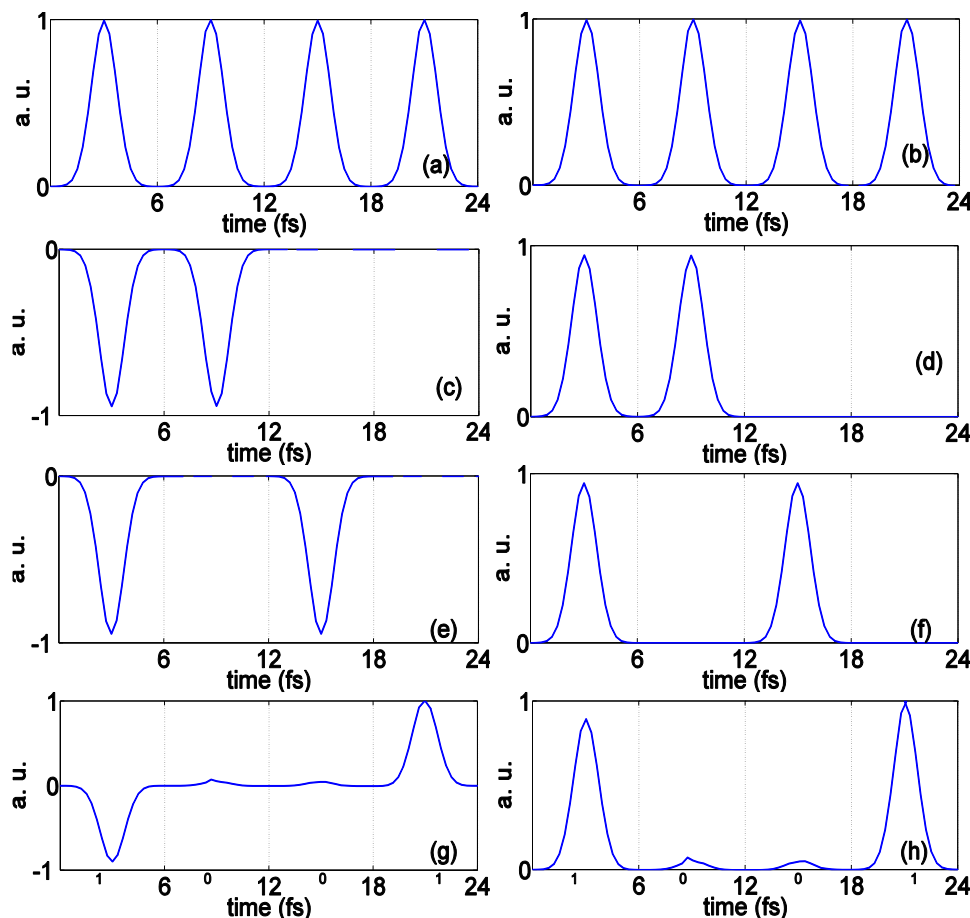


Fig. 7. Temporal view of the concerned signals in the XNOR gate; a and b) amplitude and intensity of the control Field, c and d) amplitude and intensity of the first input signal, e and f) amplitude and intensity of the second input signal, g and h) amplitude and intensity of the output signal.

4. Conclusions

The electro-optic property of the substrate material of the *2DPhC* cavities is deployed in optical tunable *CNOT* (*XOR*) and *XNOR* gates functions achievement. Where, we applied Gaussian pulse signals at the central wavelength of 1561.8nm as input signals for *L7* and *L8* cavities. Where, the resonance wavelength of the cavity can reduce from 1561.8nm to 1538.8nm by increasing the applied voltage to the each cavity. At any wavelength within the tunable range, a π radian phase difference is produced between the cavities output signals. In summary, for retaining this phase difference at the output of the *PhC* structure, as the logic gate output, we got need for an extra phase difference ($\pi/3$) between input and control signals of the logic gate. Also, by temporal simulation of the designed logic gates, we demonstrated an ultra-fast logic operation and show their high potential as an appropriate candidate for applications such as tunable optical integrated circuits and optical processors.

References

- [1] F. Akihiro, T. Takasumi Opt. Exp. **22**, 4466(2014).
- [2] X. Susan Christina, A.P. Kabilan Pho. Sens. **2**, 173(2012).
- [3] N. Morteza, A. Amin, R. Reza, Kh.Reza, Jour. of Phy. :Conf. series **350**, 1(2007).
- [4] L.Qiang, O.Zhengbiao, J.W. Chih, P. L.Chung, C.W. Jong, Opt. Exp. **16**, 18992(2008).

- [5] M.I. Babak, A.T. Tahereh, G. Nosrat, R.M.J. Ali, J. Opt. Soc. Am. B **26**, 1097(2009).
- [6] L.Wen-Piao, H.Yu-Fang, , K. Han-Lung American Journal of Modern Physics **2**, 144(2013).
- [7] B.Jibo, W.Junqin, J.Junzhen, C.Xiyao, L.Hui, Q.Yishen, Q. Zexuan, Applied Optics **48**, 6923(2009).
- [8] A.Parisa, G. Nosrat, IEEE 2008,p. 170.
- [9] A.Parisa, G.Nosrat, J. Opt. Soc. Am. B **26**, 10(2008).
- [10] A. Parisa, G. Nosrat, J. Opt. A: Pure Appl. Opt. **11**, 1(2009).
- [11] W.Chen, L.Zhi-Yuan, The frontiers of phys. **103**, 64001(2013).
- [12] F.Yulan, Xiaoyong, H.; Qihuang, G. phys.Lett.A 377, 329 (2013).
- [13] L.Ye, Q.Fei, M. Zi-Ming, Z.Fei, M.Qing-He, Li. Zhi-Yuan, Opt. Exp. **19**, 1945 (2011).
- [14] Y. Liu, F. Qin, Z. Y. Wei, Q. B. Meng, D. Z. Zhang, Z.Y. Li, Appl. Phys. Lett. **95**,(13) 1116(2009).
- [15] Y. Liu, X. Hu, D. Zhang, B. Cheng, D. Zhang, Q. Meng, Appl. Phys. Lett.**86**, 151102(2005).
- [16] X. Hu, Y. Liu, J. Tian, B. Cheng, D. Zhang, Appl. Phys. Lett.**86**, 121102(2005).
- [17] C. Y. Liu, L. W. Chen, IEEE Photonics Technol. Lett. **16**, 18491851(2004).
- [18] C. Y. Liu, L. W. Chen, Opt. Commun. **256**, 114(2005).
- [19] C. Y. Liu, L. W. Chen, Opt. Express **12**, 2616(2004).
- [20] C. Y. Liu, L. W. Chen , Phys. Rev. B: Condens. Matter Mater. Phys. **72**, 045133(2005).
- [21] C. Y. Liu, L. W. Chen, Phys. E (Amsterdam, Neth.) **35**, 173(2006).
- [22] J. Amet, G. Ulliac, F. I. Baida, M. P. Berna, Appl. Phys. Lett. **96**, 103111(2010).
- [23] H. Altug, H. Vuckovic, Appl. Phys. Lett. **86**, 1(2005).
- [24] M. S. Moreolo, V. Morra, G. Cincotti, Journal of Optics A: Pure and Applied Optics 2008, 10.
- [25] D. Yang; X. Wang, H. Tian, Y. Ji, Optica Applicata**41**, 753(2011).